

Docket No.: M4065.165/P165  
Micron Ref.: 98-0427.00/US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
APPLICATION FOR U.S. LETTERS PATENT

Title:

**METHOD FOR ETCHING DOPED POLYSILICON WITH HIGH  
SELECTIVITY TO UNDOPED POLYSILICON**

Inventors:

**Garry A. MERCALDI**

**Donald L. YATES**

Dickstein, Shapiro, Morin &  
Oshinsky LLP  
Suite 400  
2101 L Street, N.W.  
Washington, D.C. 20037  
(202) 785-9700

## **METHOD FOR ETCHING DOPED POLYSILICON WITH HIGH SELECTIVITY TO UNDOPED POLYSILICON**

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### **Field of the Invention**

The present invention relates to the field of chemical etching and, in particular, to wet chemical etching of substrates, such as a silicon semiconductor structures, with improved selectivity. The process is particularly adapted for use in producing improved pattern definition in the etching of silicon structures.

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### **Background of the Invention**

As the demand for ever-smaller silicon devices continues, and as resolution continues below the sub-micron level, the need for uniform and precise micromachining is increasing. The manufacture of semiconductor integrated circuits typically involves highly complex, time consuming and costly processes which, with continually narrower line width requirements, must be achieved with an ever increasing degree of precision. Within such processes the etching of semiconductor material (e.g., silicon) often entails the use of a chemical bath to which a patterned semiconductor material is exposed, so as to etch selectively certain portions of the surface of a wafer. In a typical chemical etch process, both the rate of etch and the selectivity of etch are parameters critical to the successful formation of an intended substrate geometry.

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When the etchant is applied to the semiconductor material (e.g., silicon), the amount of etching performed is dependent upon the etch rate, the length of time the etchant is applied and other physical factors. The direction of the etch is determined by the degree to which the selected etching process is isotropic or anisotropic. Etching which proceeds in all directions at the same rate is said to be "isotropic." By definition, any etching that is not isotropic is anisotropic. The etching of structures from polysilicon requires a process having a high degree of selectivity and anisotropy, without damaging the structure being formed. The problem is exacerbated by the requirements of sub-micron geometries. Wet etching, for example, is isotropic. Many etching processes typically fall between the extremes of being isotropic and completely anisotropic and, therefore, some unwanted etching is common even under the best conditions of prior etching processes.

Another solution is to make an isotropic etchant "selective." An etching process may be made "selective" to one surface over another based upon a variety of characteristics of the material being etched. These characteristics are termed "etch rate varying characteristics" and include, but are not limited to, chemical composition, the ratio of the etchant components, dopant levels, density, post-deposition annealing conditions, deposition temperature and deposition pressure. By making an etching process or etchant "selective" to a particular surface or composition, the process or etchant will etch that particular surface or composition less rapidly than the surrounding surface or other material with which the etchant may come into contact. Accordingly, when an etchant is

selective for a heavily doped material such as heavily boron doped BPSG, the etchant will etch layers of material containing heavily boron doped BPSG less quickly than other layers not similarly composed.

Various etching techniques and compositions have been used in the fabrication of semiconductor integrated circuits. For example, U.S. Pat. No. 3,677,848 (Irwin et al.) describes an etching method which immerses a semiconductor body into a mixture of hydrofluoric acid, nitric acid, acetic acid and either sodium chlorite or sodium nitrite. The patent describes that the mixture will etch a silicon or germanium body at a rate of 0.00033 to 0.001 inch/minute. However, the mixture does not have any selectivity to different parts of the silicon or germanium body.

U.S. Pat. No. 4,026,733 (Owen, III et al.) describes a process for defining polycrystalline silicon patterns from a masking member. The patent describes that self-limiting etching of the silicon is achieved through an etchant that discriminates between doped and undoped silicon. The selective etchant solution of the '733 patent includes hydrofluoric acid, nitric acid and acetic acid.

U.S. Pat. No. 4,142,926 (Morgan) describes a process for fabricating a double layer polycrystalline silicon structure for a metal-oxide-semiconductor (MOS) integrated circuit by employing a selective etchant which discriminates between silicon layers. The '926 patent recites that the selective etching solution includes hydrofluoric acid, nitric acid and acetic acid and selectively etches phosphorous doped polycrystalline silicon.

U.S. Pat. No. 4,681,657 (Hwang et al.) describes an etchant composition and method for resistivity specific etching of doped silicon films which overlie intrinsic or lightly doped crystal regions. The etching method includes contacting a semiconductor body with a mixture of hydrofluoric acid, nitric acid and predominantly acetic acid/water mixture.

U.S. Pat. No. 5,532,182 (Woo) describes a process for fabricating stacked DRAM capacitors. The patent describes an etching solution including nitric acid, acetic acid, hydrofluoric acid and deionized water in a ratio of from 30:3:0.5:15 to 30:3:1:15.5 to selectively etched doped silicon.

U.S. Pat. No. 5,637,523 (Fazan et al.) describes a method of forming a capacitor. The patent recites that an etching solution including 0.7% of hydrofluoric acid, 50 to 54% nitric acid and 2.6% acetic acid selectively etched doped polycrystalline silicon. Additionally, the patent describes a selective etching solution including 0.53% HF, 37.63% HNO<sub>3</sub>, 22.58% CH<sub>3</sub>COOH and 4.21% NH<sub>4</sub>F with the remainder deionized water achieved a selectivity of greater than 3:1 undoped to doped silicon.

As discussed above, these proposed solutions and additional proposed solutions have been attempted to selectively etch semiconductor materials in the manufacture of integrated circuits with high selectivity. These additional attempts include using anisotropic etchants, tailoring the chemical concentrations of known isotropic etching solutions, and tailoring the physical environments of

etching apparatus. However, these prior methods have not provided an etching composition and method having a high selectivity.

### **Summary of the Invention**

5           The present invention has advantages over the previous methods in that selectivity of the etching of doped polysilicon is increased with respect to undoped polysilicon. The present invention provides an etching mixture including a polyhydric or lower alkyl alcohol together with a combination of two or more acids. Preferably the etching composition of the present invention is a  
10           mixture of a glycol, nitric acid and hydrofluoric acid, with propylene glycol being preferred. By providing an etching formulation which has increased selectivity of doped polysilicon to undoped polysilicon, the present invention provides an efficient integrated circuit fabrication process without requiring time consuming and costly processing modifications to the etching apparatus or production  
15           apparatus.

### **Brief Description of the Drawings**

Fig. 1 is a diagrammatic cross-sectional view of a portion of a semiconductor wafer at an intermediate processing step.

20           Fig. 2 is a diagrammatic cross-sectional view of a portion of a semiconductor wafer at a processing step subsequent to that shown in Fig. 1.

Fig. 3 is a scanning electron microscope (SEM) photograph of a silicon substrate processed according to the present invention.

Fig. 4 is a SEM photograph of a silicon substrate processed according to the present invention.

### **Detailed Description of the Preferred Embodiments**

In the following detailed description, reference is made to the accompanying drawings which form a part hereof, and in which is shown by way of illustration specific embodiments in which the invention may be practiced. These embodiments are described in sufficient detail to enable those skilled in the art to practice the invention, and it is to be understood that other embodiments may be utilized, and that structural, logical and electrical changes may be made without departing from the spirit and scope of the present invention.

The terms "wafer" and "substrate" are to be understood as including silicon-on-insulator (SOI) or silicon-on-sapphire (SOS) technology, doped and undoped semiconductors, epitaxial layers of silicon supported by a base semiconductor foundation, and other semiconductor structures. Furthermore, when reference is made to a "wafer" or "substrate" in the following description, previous process steps may have been utilized to form regions or junctions in the

base semiconductor structure or foundation. In addition, the semiconductor need not be silicon-based, but could be based on silicon-germanium, germanium, or gallium arsenide.

The selective etching composition according to the present invention includes an alcoholic component and an acidic component. The selective etching composition optimally includes a polyhydric alcohol or C<sub>2</sub>-C<sub>6</sub> alcohol together with a combination of two or more acids. The selective etching composition of the present invention preferably includes a predominant amount of the alcohol and is also preferably formulated to be a non-aqueous composition, that is a non-water based composition.

The polyhydric alcohols useful in the present invention include any known polyhydric alcohols such as glycerol (also known as glycerin), polyalkylene glycols, and more preferably alkylene polyols and their derivatives, including ethylene glycol, propylene glycol, butylene glycol, dipropylene glycol, polypropylene glycol, polyethylene glycol and derivatives thereof, sorbitol, hydroxypropyl sorbitol, hexylene glycol, 1,3-dibutylene glycol, 1,2,6-hexanetriol, 1,5-pentanediol, ethoxylated glycerol, propoxylated glycerol and mixtures thereof. The C<sub>2</sub>-C<sub>6</sub> alcohols useful in the present invention are those such as ethanol, propanol, isopropanol, iso-butanol, n-butanol and the like as well as mixtures thereof. Preferably, the alcoholic component of the selective etching composition is a polyhydric alcohol. A preferred polyhydric alcohol is a glycol, especially those such as ethylene glycol and propylene glycol.



The acidic component of the selective etching composition of the present invention includes two or more inorganic acids. Examples of inorganic acids suitable for the present invention are those such as hydrofluoric acid, nitric acid, phosphoric acid, sulfuric acid, boric acid, carbonic acid, perchloric acid and sulfurous acid. Preferably the acidic component of the selective etching composition includes hydrofluoric acid and nitric acid.

The selective etching composition of the present invention is comprised primarily of the alcoholic component of the composition. In a preferred embodiment of the invention, the ratio of alcohol to nitric acid to hydrofluoric acid ranges from about 10-50:5-40:1, more preferably from about 20-40:10-30:1. In a most preferred embodiment, the selective etching composition according to the present invention is comprised of propylene glycol, nitric acid and hydrofluoric acid in a ratio of about 30:20:1.

The wafers processed according to the present invention were etched by immersing them in a constantly stirred bath of the etchant at a temperature of from about -10°C to about 70°C, preferably at about 35°C. Stirring of the etching solution can be ultrasonic or by means of a magnetic stirrer. Alternatively, a flow-through agitation or a spray-type etcher could be used.

The selective etching composition of the present invention selectively removes doped materials from the substrate to the exclusion of undoped materials. That is, the selective etching composition of the present invention will rapidly etch materials such as BPSG, doped polysilicon, doped monocrystalline

silicon, doped amorphous silicon, doped pseudocrystalline silicon, doped germanium, doped gallium arsenide while not significantly etching undoped materials.

The invention will now be further described with reference to the figures. Reference is now made to Fig. 1. A semiconductor wafer fragment at an intermediate processing step is indicated generally by reference numeral 100. The semiconductor wafer 100 is comprised of a bulk silicon substrate 112 with field isolation oxide regions 114 and doped active area 120 formed therein. A BPSG layer 142 has been deposited on the substrate. A plug 146 of doped polysilicon conductive material has been deposited in plug openings 150. A doped polysilicon conductive layer 152 has been deposited in plug opening 150. An HSG layer 153 has been deposited over the doped polysilicon conductive layer 152 and the portions of the doped polysilicon conductive layer 152 and the HSG layer 153 above the top of the BPSG layer 142 have been removed through CMP. It should be understood, however that the plug 146 and the doped polysilicon conductive layer 152 may also be formed of doped amorphous silicon, monocrystalline silicon and amorphous silicon.

Referring now to Fig. 2, the BPSG layer 142 and the doped polysilicon conductive layer 152 are then etched back around the HSG layer 153 by immersing the wafer 100 in a etching solution which comprises propylene glycol, nitric acid and hydrofluoric acid in a ratio of 30:20:1 for about 45 seconds in a 35°C etching bath. The etchant selectively removed the BPSG layer

142 and the doped polysilicon layer 152 as illustrated in the figure, but did not applicably remove the undoped HSG layer 153.

The invention is further explained with reference to the following example. This invention is not intended to be limited by the particular example described below. A polysilicon wafer including in-situ doped polysilicon which has been annealed, undoped polysilicon, thermal silicon oxide and rich BPSG which had been annealed and densified was placed in a static etching bath at 35°C with a composition of propylene glycol, nitric acid and hydrofluoric acid in a 30:20:1 ratio for a predetermined time. The etching rate of the silicon substrate was measured as shown in Table 1 below.

Material Etched	Etch Rate (Å/min)
Doped Polysilicon	694.00
Undoped Polysilicon	9.52
Thermal Silicon Oxide	49.80
BPSG (annealed and densified)	1410.67

TABLE 1

Based on the etching rates of the propylene glycol, nitric acid, hydrofluoric acid etchant measured above, the selectivity ratio of the etchant was measured as a ratio of etching rates as shown in Table 2 below.

	Doped Polysilicon	Undoped Polysilicon	Thermal Silicon Oxide	BPSG
Doped Polysilicon	-	73:1	14:1	1:2
Undoped Polysilicon	-	-	1:5	1:148
Therman Silicon Oxide	-	-	-	1:28
BPSG	-	-	-	-

TABLE 2

Table 2 shows that the selective etching composition of the present invention achieved a selectivity ratio of greater than 70:1, doped polysilicon to undoped polysilicon, and a selectivity ratio of greater than 140:1, BPSG to undoped polysilicon. These examples show the unexpectedly high selectivity of the composition of the present invention.

Reference is now made to Figures 3-4. These figures show SEM photographs of a silicon wafer in which blanket polysilicon films (030 ISD annealed and 256 undoped) were immersed in a static etching bath at 35°C with a composition of propylene glycol, nitric acid and hydrofluoric acid in a 30:20:1 ratio for a predetermined time. Fig. 3 is a SEM photograph magnified a 8724 times while Fig. 4 is magnified at 30,000 times. As can be seen from these figures, the complete removal of doped polysilicon is evident. All that remains in the undoped polysilicon showing the high selectivity of the composition of the present invention.

The selective etchant composition according to the present invention can be used in any application requiring the removal of a doped polysilicon layer

overlying intrinsic or lightly doped silicon. The intrinsic or lightly doped silicon need not be single crystal silicon, it can be intrinsic or lightly doped polysilicon. The particular composition chosen will be a function of the thickness of the polysilicon to be removed, and will allow a suitable etch rate for process control purposes. The etchant can also be used in applications where the underlying layer is silicon dioxide or silicon nitride or where a silicon dioxide or silicon nitride layer is otherwise exposed to the etchant for limited times.

It should again be noted that although the invention has been described with specific reference to silicon substrates, the invention has broader applicability and may be used in any process where selective etching is required. Similarly, the process described above is but one method of many that could be used. Accordingly, the above description and accompanying drawings are only illustrative of preferred embodiments which can achieve and provide the objects, features and advantages of the present invention. It is not intended that the invention be limited to the embodiments shown and described in detail herein. The invention is only limited by the spirit and scope of the following claims.

What is claimed as new and desired to be protected by Letters Patent of the United States is: